



2/27/2002

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## **Errata: CS98100 Rev. B**

(Reference CS98100 Data Sheet revision DS552PP3 dated Feb '02)

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The following errata have been identified on our tester, but have not caused any field problems.

1) SDRAM interface A.C. timing parameters:

tmdow - DR\_D output delay from DR\_CKO

datasheet limit: 9 ns (min)

relax to: 10 ns (min)

2) Digital Audio Interface A.C. timing parameters:

taxcl - AUD\_XCLK low time

datasheet limit: 40% (min)

relax to: 47% (min)

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If there are any questions concerning this information,  
Please contact: <http://www.crystal.com/support/engineering/index.cfm>

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